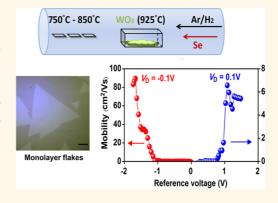
Large-Area Synthesis of Highly Crystalline WSe₂ Monolayers and Device Applications

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ABSTRACT The monolayer transition metal dichalcogenides have recently attracted much attention owing to their potential in valleytronics, flexible and lowpower electronics, and optoelectronic devices. Recent reports have demonstrated the growth of large-size two-dimensional MoS₂ layers by the sulfurization of molybdenum oxides. However, the growth of a transition metal selenide monolayer has still been a challenge. Here we report that the introduction of hydrogen in the reaction chamber helps to activate the selenization of WO₃, where large-size WSe₂ monolayer flakes or thin films can be successfully grown. The top-gated field-effect transistors based on WSe₂ monolayers using ionic gels as the dielectrics exhibit ambipolar characteristics, where the hole and electron mobility values are up to 90 and 7 cm²/Vs, respectively. These films can be transferred onto arbitrary substrates, which may inspire research



efforts to explore their properties and applications. The resistor-loaded inverter based on a WSe₂ film, with a gain of \sim 13, further demonstrates its applicability for logic-circuit integrations.

KEYWORDS: transition metal dichalcogenides · tungsten diselenides · layered materials · transistors · inverters · two-dimensional materials

ransition metal dichalcogenides (TMDs) have attracted much attention due to their two-dimensional (2-d) layer structure, where a sheet of metal atoms is sandwiched between two sheets of chalcogens by covalent interaction. Depending on the selection of the metal, these layered materials may exhibit superconducting, metallic, or semiconducting properties.^{1–4} Moreover, the electronic structures of the semiconducting TMDs such as molybdenum disulfide (MoS₂), tungsten disulfide (WS₂), and tungsten diselenide (WSe₂) vary with their thickness. For example, the optical transition of MoS₂ transforms from the indirect gap in a bulk form to the direct gap when it is a monolayer sheet.^{5–13} These monolayer TMD films are highly bendable and thus promising for flexible electronics.¹⁴ Another important feature of the 2-d TMD films is that their properties are

sensitive to the electrostatic interaction or a gate voltage.^{15,16} Recently, it has been demonstrated that the field-effect transistors fabricated with the MoS₂ thin layers exhibit an excellent on/off current ratio and a reasonably good carrier mobility.^{17,18} So far, most of the transistors based on either exfoliated or synthetic MoS₂ layers show an n-type behavior. Very recent reports have demonstrated the formation of MoS₂ p-n junction devices by using electric doublelayer gating^{19,20} or creating p-doped MoS₂ by efficient plasma treatment.^{21,22} At the same time, the research for an intrinsically p-type TMD monolayer becomes urgent for realizing complementary digital logic applications. Actually, the p-type transistor behaviors have been observed in bulk and mechanically exfoliated monolayer or few-layer WSe₂ flakes.^{23,24} Recent studies have made significant advances in

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Received for review November 4, 2013 and accepted December 14, 2013.

Published online December 14, 2013 10.1021/nn405719x

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VOL.8 • NO.1 • 923-930 • 2014



understanding the role of contact metals on the electrical characteristics of the transistors made from exfoliated WSe₂ flakes.^{23,25} However, the large-scale device applications still rely on the breakthrough in the growth of large-size WSe₂ thin layers. Early approaches including the thermal decomposition of WSe_{3} ,²⁶ direct heating of the mixture of W and Se elements, 27-31 vapor transport growth of WSe_2 ,³² and the reaction of WO_3 fine powders with H_2Se^{33} or Se^{34} have led to successful growth of WSe2 crystals or fullerene-like particles but not atomically thin layers. Recent progress on the growth of monolayer MoS₂ by the sulfurization of molybdenum oxides^{35–38} in vapor phases, where the formation of MoS₂ atomically thin layers is based on the reaction of MoO₃ and sulfur vapors, has shed light on the large-area preparation of other TMD monolayer materials. In this contribution, we report that the introduction of hydrogen in the reaction environment is necessary for the growth of a highly crystalline WSe2 monolayer using the vapor phase reaction of WO₃ and Se. The field-effect transistors based on the as-grown WSe2 monolayers exhibit ambipolar behaviors with high mobility values in both p- and n-channels.

RESULTS AND DISCUSSION

Figure 1a schematically illustrates our experimental setup for growing thin WSe₂ layers by the selenization of WO₃ powders in a hot-wall chemical vapor deposition (CVD) chamber. The morphology of the WSe₂ grown on the sapphire substrates varies with the substrate temperatures. The optical micrograph (OM) in Figure 1b shows that the WSe₂ grown at 850 °C exhibits a triangular shape, and the lateral size of each triangle is larger than 10 μ m and up to 50 μ m, where the lateral size may have a limit due to the high energy caused by the lattice mismatch between the WSe₂ and sapphire substrates. Meanwhile, the sparsely distributed triangles indicate that the nucleation density of the WSe₂ is low. Figure 1c is the OM image for the WSe₂ grown at a lower temperature at 750 °C, where many small WSe₂ domains merge to form a continuous film. The domain size of the WSe₂ formed at 750 °C is around several to 5 μ m, as shown in the SEM image in Supporting Information Figure S1 for the nearly merged WSe₂ grown at 750 °C. These observations are reasonable since a lower growth temperature typically results in a higher nucleation density. It is noteworthy that the orientation of these small WSe₂ triangles grown at 750 °C aligns to a certain direction, implying the expitaxial growth feature of the process. Figure 1d shows the atomic force microscopic (AFM) image for the WSe₂ triangle grown at 850 °C, where the thickness of 0.73 nm of the film is in agreement with the reported thickness for mechanically exfoliated WSe2 monolayers.^{23,25} The AFM image and crosssection profile in Figure S2 obtained for the WSe₂ film

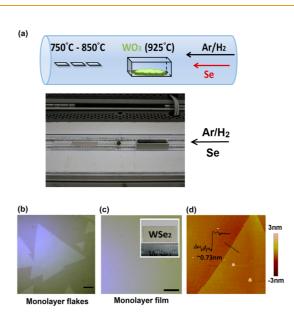


Figure 1. (a) Schematic illustration for the growth of WSe₂ layers on sapphire substrates by the reaction of WO₃ and Se powders in a CVD furnace. A photo of the setup is also shown. (b,c) Optical microscopy images of the WSe₂ mono-layer flakes and monolayer film grown at 850 and 750 °C, respectively. Scale bar is 10 μ m in length. The inset in (c) shows the photograph of a uniform monolayer film grown on a double side polished sapphire substrate. (d) AFM image of a WSe₂ monolayer flake grown at 850 °C on a sapphire substrate.

grown at 750 °C also prove that the continuous film is a monolayer. For the WSe₂ flakes grown at 850 °C, most of the flakes are characterized as monolayers, but we still occasionally observe the growth of second layer WSe₂ on top of some large monolayer flakes, as shown in the OM in Figure 2a. The AFM cross-sectional height profile for the sample shown in Figure S3 confirms that the additional layer on top is a monolayer. Note that we have also performed the WSe₂ growth on SiO₂ substrates, but the same growth method preferentially leads to particle-like structures, not a two-dimensional film. It is likely that the growth of WSe₂ has strong surface sensitivity.

Raman spectra for the monolayer and bilayer WSe₂ excited by a 473 nm laser are shown in Figure 2b, where the two characteristic peaks for monolayer WSe₂ at 248 cm⁻¹, assigned to E_{2q}^{1} mode, and 259 cm⁻¹, assigned to A_{1g} mode, are observed.^{39,40} For the bilayer WSe₂, the A_{1a} mode slightly left shifts compared with that of monolayer WSe2, which is consistent with the observation in ref 39. Other high-energy bands at 358.5 and 373.5 cm⁻¹, attributed to the $2E_{1a}$ and $A_{1a}+LA$ modes, are also identified.⁴¹⁻⁴³ Most importantly, the Raman band at 308 cm^{-1} , which has been reported to be related to the interlayer interaction,⁴⁴ is not observed in our WSe₂ monolayer. Instead, this characteristic peak exhibits in the Raman spectrum for our bilayer WSe₂, consistent with the results in ref 39. Figure 2c displays the optical absorption spectrum for the monolayer WSe₂ film. The excitonic absorption

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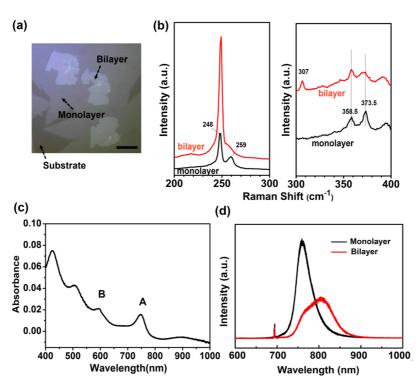


Figure 2. (a) Optical microscopic image of the second layer WSe₂ on top of some large monolayer flakes. Scale bar is $10 \,\mu$ m in length. (b) Raman spectra for the monolayer and bilayer WSe₂, obtained in a confocal Raman spectrometer excited by a 473 nm laser. (c) Optical absorption spectrum for the continuous WSe₂ film. (d) Photoluminescence spectra for the CVD WSe₂ monolayer and bilayer, obtained in a microscopic PL system (excitation wavelength 532 nm).

peaks A and B, arising from direct gap transitions at the K point, are identified at around 750 and 595 nm respectively, agreeing well with previous reports.^{45–47} Figure 2d compares the PL spectra (excited by a 532 nm laser) of monolayer and bilayer WSe₂ areas. The PL spectrum for the monolayer WSe₂ flake exhibits a strong emission at ~760 nm corresponding to the A

excitonic absorption, whereas the emission intensity of the same peak is much lower in the bilayer WSe₂. Moreover, in addition to the A exciton peak, another peak at a longer wavelength (~806 nm), attributed to the indirect band gap emission, is observed for the bilayer WSe₂. It is noted that the indirect gap emission is absent in a monolayer, and the distinctly strong emission from a monolayer is in good agreement with the recent report.^{48–50} All of these PL observations further confirm the layer number assignment for our CVD WSe₂ samples.

X-ray photoemission spectroscopy (XPS) was used to measure the binding energies of the W and Se in our CVD WSe₂ monolayer film. Figure 3a,b shows the binding energy profiles for W 4f and Se 1s. The peaks at 32.8 and 35.0 eV are attributed to the doublet W 4f_{7/2} and W 4f_{5/2} binding energies, respectively. The peaks at 55 and 55.9 eV correspond to the Se 3d_{5/2} and Se 3d binding energies. All of these results are consistent with the reported values for the WSe₂ crystal.^{51,52} It is noted that Figure 3a does show the presence of weak peaks for WO₃ at 35.8 and 38.0 eV (the doublet W 4f_{7/2} and W 4f_{5/2} for WO₃, respectively). This is caused by the occasionally deposited WO₃ particles on the surface of WSe₂. The domain edge of the WSe₂ may also contribute to the WO₃ signals. Figure S4 displays the SEM image showing the formation of some WO₃ particles during the growth of WSe₂ film. Nevertheless, the ratio between W and Se elements obtained from the integrated peak area after excluding the WO₃ part is 1:1.988, suggesting that our CVD WSe₂ exhibits reasonably good stoichiometry.

The tunneling electron microscopy (TEM) image in Figure 3c shows the periodic atom arrangement of the WSe₂ monolayer, demonstrating that the CVD WSe₂ film is highly crystalline. The inset shows the selected area electron diffraction (SAED) pattern taken with an aperture size of ~160 nm for the sample. Figure 3d is the magnified TEM image for the area squared by dashed lines in Figure 3c, where the high-resolution TEM image and the corresponding SAED pattern with [001] zone axis (inset of Figure 3c) reveal the hexagonal lattice structure with the lattice spacing of 0.38 and 0.33 nm assigned to the (100) and (110) planes.⁵³

It is noteworthy pointing out that, for the synthesis of a MoS_2 monolayer using the same method,³⁵ the sulfurization of MoO_3 is typically performed at 650 °C in an Ar environment, where the chemical reaction is described as

$$2\text{MoO}_3 + 7\text{S} \rightarrow 2\text{MoS}_2 + 3\text{SO}_2 \tag{1}$$

The introduction of a reducer such as hydrogen in the reaction atmosphere is not necessary. However, the

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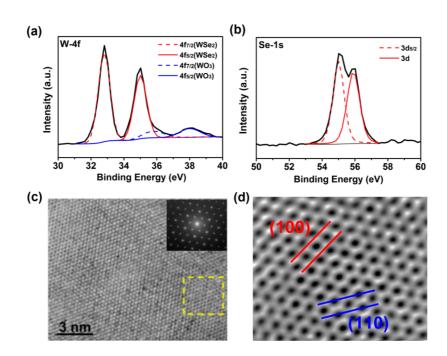


Figure 3. (a,b) XPS spectra of the WSe₂ monolayer film, where the (a) W 4f and (b) Se 1s binding energies are identified. The two peaks at 32.8 and 35.0 eV are attributed to the doublet W $4f_{7/2}$ and W $4f_{5/2}$. Note that the peaks at 35.8 and 38.0 eV are attributed to the doublet W $4f_{7/2}$ and W $4f_{5/2}$. Note that the peaks at 35.8 and 38.0 eV are attributed to the doublet W $4f_{7/2}$ and W $4f_{5/2}$ for WO₃. The binding energy at 55 and 55.9 eV can be assigned to the Se $3d_{5/2}$ and Se 3d binding energies, respectively. (c) High-resolution TEM image of WSe₂ monolayer with an inset showing its SAED pattern. (d) Enlarged TEM image for the marked area in (c).

chemical reactivity of Se is much lower than that of S, and thus a strong reducer like hydrogen is required in the selenization reaction of WO₃. In our experiments, we also observe that no WSe_2 is obtained if no hydrogen gas is introduced in the chamber. The thermodynamic calculations have also corroborated that the selenization reaction of WO₃ is only possible with the presence of hydrogen.³⁴ The chemical reaction occurring during the selenization of WO₃ is as follows:

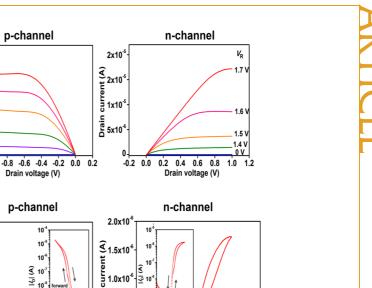
$$WO_3 + 3Se + H_2 \rightarrow WSe_2 + H_2O + SeO_2$$
 (2)

Note that if the hydrogen ratio is too high, the reduction of WO₃ to W metal proceeds very efficiently. The WO₃ evaporation rate thus becomes relatively slow, which does not favor the formation of WSe₂. The Ar/H₂ ratio of 4:1 was an optimized parameter based on our experiments and above rationale.

To evaluate the quality of the synthetic WSe₂ monolayer, we measured the electrical properties of the electric double-layer transistors (EDLT) based on the as-grown WSe₂ monolayer film on sapphire substrates. EDLT is known as a good method to determine the mobility at a high carrier concentration, which is not easily accessed by solid insulator gating. Figure 4a shows the OM image for the EDLT device top view, where the channel width and length are 668 and 1225 μ m, respectively. Figure 4b shows the output characteristics for both the p- and n-channels at various reference voltages (V_R) as specified in graphs. Note that the V_R is the measured voltage between the electrolytes and WSe₂, that is, the voltage for the electric double layer on WSe₂ surfaces, and it is normally smaller than the applied gate voltage on the top Pt metal since the gate voltage is partially consumed by the electric double layer on the gate electrode. The correlation between the applied gate voltage and the reference voltage is shown in Figure S5. It is observed that the source-drain current exhibits an obvious saturation regime for all curves. To obtain the capacitance of the WSe₂/ion gel surface, the source and drain electrodes are short-circuited, and two terminal measurements across the source/drain and top-gate electrodes are carried out. The frequency range is set to 10^{-3} – 10^{5} Hz. As shown in Figure S6, the phase angle becomes approximately -90° , and the estimated capacitance is almost constant when the frequency is <100 Hz. Especially, for dc voltage dependence of ion gel capacitance, the frequency is set to 10 Hz with an ac voltage amplitude of 5 mV, and dc voltage is applied from -3.2 to 3.4 V. The measurement result for the specific capacitance at various reference voltages is shown in Figure 4c. The dependence of the capacitance on V_R reveals a step-function-like profile for both p-and n-channels, which is reasonably explained by competition of geometrical and quantum capacitances. Owing to the high ionic concentration of the ion gel and the large specific surface area of the 2-d TMD film, the measured capacitance consists of two major contributions: the quantum capacitance and the interfacial capacitance arising from the electric double layer formed by ions at the WSe₂-ion gel interface, which can be modeled as two capacitors in series.⁵⁴

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*V*_D = 0.1V

2.0

1.5

Figure 4. (a) Optical micrograph of the top view for the WSe₂ EDLT device, where the top and bottom photos were taken before and after the ion gel/top gate deposition. (b) Output characteristics of the WSe₂ EDLT device. (c) Specific capacitance and carrier mobility values measured at various reference voltages. The drain current level was maintained at \pm 0.1 V, which was in the linear regime. (d) Typical transfer curves for the EDLT device, where the inset in each graph shows the transfer curve plotted in a log scale.

۷_D = -0.1۱

-1.5

p-channel

Drain voltage (V)

p-channel

10

10

10

10

10

V_P (V)

-0.5

Drain

0.0

5.0x10⁻

0.0

0.0

K. (V)

1.0

Reference voltage (V)

0.5

₹ 10⁻¹

19

-1.0

Reference voltage (V)

(b)

-1x10

€-8x10⁴

-6x10

-4x10

-2x10

curren

Drain

(d) 4x10⁻⁶

> ₹ -1.6x10

current -1.2x10

Drain -8.0x10

-2.0x10

-4.0x10

0.0

-2.0

8x10⁻

- 6x10⁻⁶

2x10⁻

 $V_{\rm D} = 0.1 \rm V$

 $V_{\rm D} = 0.1 \rm V$

 $V_{\rm R}$

-1.6 V

-1.5 V

-1.4 V

-1.3 V

-1.2 V

0 \ 0 -1.2 -1.0

The measured capacitance is dominated by the quantum capacitance when the V_R is inside the E_q of WSe₂, whereas the capacitance is governed by the geometrical capacitance when the V_R is outside the E_{a} . Note that the effect of in-gap states is negligible due to the huge geometrical capacitance of the ion gel. Based on this argument, the E_{α} of WSe₂ can be estimated to be the difference between the rising onset points in p-channel (-0.82 V) and n-channel (0.79 V), that is, \sim 1.61 V, where the number approximately agrees with the energy band gap ($E_{\rm g}$ \sim 1.6 eV) of WSe₂ monolayer. Moreover, the field-effect mobility was calculated by the standard formula for the linear region, μ = (L/WC_iV_D) × $(\Delta I_D/\Delta V_R)$, where μ is the field-effect mobility, W is the channel width, $V_{\rm D}$ is the drain voltage, C_i is the measured specific capacitance of the ion gel, L is the channel length, and $I_{\rm D}$ is the drain current.

Figure 4d displays the p- and n-transfer curves (drain current vs $V_{\rm R}$) of a WSe₂ monolayer EDLT at the applied drain voltage $V_{\rm D} = -0.1$ and 0.1 V, respectively. Consistent with the report for thick WSe₂ crystals by Podzorov et al.,²⁴ we clearly observe an ambipolar transport behavior with ohmic-like current-voltage characteristics. The inset in each graph replots the drain current from a linear to a log scale, and the on-off current ratio is as high as 10⁵ and 10⁴ for p- and n-channels. The reference voltage dependence of carrier mobilities from capacitance measurements in Figure 4c also shows a step-like profile. The highest carrier mobility is 90 cm²/Vs for the hole transport and

(a)

(c)

F/cm

Capacitance

Mobility (cm²/Vs)

8x10

6x10

4x10

2x10

0

100

80

60

40

20-

0

-2

 $V_{\rm D} = -0.1 V$

-0.1V

-1

0

Reference voltage (V)

7 cm²/Vs for the electron transport. It is noted that Figure S7 provides the transfer curves, mobility, and capacitance dependences on $V_{\rm R}$ performed in the saturation regime, and these results are consistent with those measured in the linear regime. Figure S8 shows the electrical characteristics for the two devices fabricated on the same WSe₂ film (spaced by 1 cm). The devices with three channel lengths (7, 10, and 6 μ m) have also been fabricated. Table S1 lists all the characteristics including $V_{\rm th}$, S, mobility, and $I_{\rm on}/I_{\rm off}$ for the eight devices we prepared. The electrical results for all devices are quite consistent. Figure 4c explores that the Ni source and drain metals form a midgap Schottky barrier with WSe₂ film. The higher hole mobility of the device is mainly because the Ni S/D electrodes are more accommodating at the p-channel Fermi level.

To examine the logic operation capability, we have fabricated a simple resistor-loaded inverter by connecting WSe₂ monolayer EDLTs in series with 500 k Ω commercial resistors. The circuit diagram and input (V_{in}) -output (V_{out}) voltage characteristics are presented in Figure 5a. Using the p-type operation of a WSe₂ monolayer EDLT with the mobility of 50 cm²/Vs and the on-off current ratio of 10⁶, ideal inverter actions were observed. The output voltage switched near $V_{in} = 1$ V, and the inverter was completely switched within 0.5 V of the V_{in} variation. The voltage gain, defined as the negative of dV_{out}/dV_{in} , plotted in Figure 5b was approximately 13. For the successful implementation of digital logics in electronic circuits

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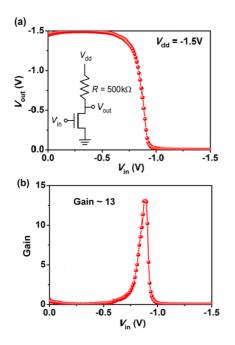


Figure 5. (a) Circuit diagram and input (V_{in}) -output (V_{out}) voltage characteristics, and (b) voltage gain (defined as the negative of dV_{out}/dV_{in}) of the resistor-loaded WSe₂ inverter.

based on any new nanomaterial, a voltage gain more than 1 is needed so that the output of one inverter

METHODS

Growth of WSe₂ Layers. The WO₃ powders (0.3 g) were placed in a ceramic boat located in the heating zone center of the furnace. The Se powders were placed in a separate ceramic boat at the upper stream side maintained at 270 °C during the reaction. The sapphire substrates for growing WSe₂ were put at the downstream side, where the Se and WO₃ vapors were brought to the targeting sapphire substrates by an Ar/H₂ flowing gas (Ar = 80 sccm, H₂ = 20 sccm, chamber pressure = 1 Torr). The center heating zone was heated to 925 °C at a ramping rate of 25 °C/min. Note that the temperature of the sapphire substrates was at ~750 to 850 °C when the center heating zone reaches 925 °C. After reaching 925 °C, the heating zone was kept for 15 min and the furnace was then naturally cooled to room temperature.

Fabrication of EDLT Devices. For the EDLT source and drain electrodes, Au contacts with Ni adhesion layers (70 nm/2 nm) were thermally deposited onto the surface of the WSe₂ films. The ion gels, a mixture of a triblock copolymer, poly(styreneblock-methyl methacrylate-block-styrene) (PS-PMMA-PS; $M_{PS} =$ 4.3 kg/mol, M_{PMMA} = 12.5 kg/mol, M_{w} = 21.1 kg/mol), and an ionic liquid, 1-ethyl-3-methylimidazolium bis(trifluoromethylsulfonyl)imide ([EMIM][TFSI]) in an ethyl propionate solution, are used as the top gate dielectrics.¹⁴ Note that the weight ratio of the polymer, ionic liquid, and solvent was maintained at 0.7:9.3:20. This solution was drop-casted onto and covered the surfaces of WSe₂ film and the source and drain electrodes. The transistor channel was then covered with a thin Pt foil (thickness of 0.03 mm) to form the top-gate electrode. Finally, a thin gold wire was inserted into the gel films, between the channel and top gate metal, as the reference electrode.

Characterizations. The AFM images were performed in a Veeco Dimension-Icon system. Raman spectra were collected in a confocal Raman system (NT-MDT). The wavelength of laser is 473 nm (2.63 eV); the spot size of the laser beam is \sim 0.5 μ m, and the spectral resolution is 3 cm⁻¹ (obtained with a

could drive the input of the next inverter in the cascade. Therefore, our inverter is readily applicable for the integration in arrays of logic gates.

CONCLUSIONS

In conclusion, we have synthesized highly crystalline and large-area WSe₂ monolayers by the gas phase reaction of WO₃ and Se powders in a hot-wall CVD chamber. It is concluded that the hydrogen gas plays an important role to activate the reaction. Triangle and single-crystalline WSe₂ monolayer flakes can be grown at 850 °C, whereas a lower temperature 750 °C results in a continuous and polycrystalline monolayer film. The WSe₂ transistors based on ion gel gating exhibit an ambipolar behavior with high carrier mobility values. The proof-of-concept WSe₂-based inverter exhibits a high gain of \sim 13, demonstrating that synthetic WSe₂ layers can serve as a building block for future logic circuits based on 2-d materials. The crystalline sapphire is a good template for WSe₂ growth. However, the sapphire substrate is not cheap; hence, the growth of 2-d materials on other substrates is urgently needed. Actually, the growth method reported here should also be applicable to other substrates such as quartz and SiO₂/Si, while more efforts are required to create good nucleation environments for WSe₂.

600 grooves/mm grating). The Si peak at 520 cm⁻¹ was used as a reference for wavenumber calibration. The WSe₂ films were transferred onto a copper grid for TEM observation. HRTEM imaging was performed on JEOL-2100F FEG-TEM operated at 100 kV. Chemical configurations were determined by X-ray photoelectron spectroscopy (XPS, Phi V6000). XPS measurements were performed with an Mg K α X-ray source on the samples. The energy calibrations were made against the C 1s peak to eliminate the charging of the sample during analysis. All electrical characterizations were performed using a semiconductor parameter analyzer (Agilent E5270) in a shield probe station inside a N₂-filled glovebox. We perform the impedance measurements using a frequency response analyzer (a Solartron 1252A frequency response analyzer with a Solartron 1296 dielectric interface controlled by ZPIot and ZView software).

Conflict of Interest: The authors declare no competing financial interest.

Acknowledgment. This research was mainly supported by Academia Sinica (IAMS and Nano program) and National Science Council Taiwan (NSC-99-2112-M-001-021-MY3 and 99-2738-M-001-001). T.T. was partially supported by the Funding Program for the Next Generation of World-Leading Researchers. Y.I. was supported by a Grant-in-Aid for Scientific Research (S) (21224009), by the FIRST Program from JSPS, and by SICORP from JST.

Supporting Information Available: Figures S1–S8 and Table S1 are included. This material is available free of charge *via* the Internet at http://pubs.acs.org.

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